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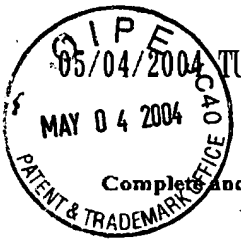
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Rhonda Dunn (Depositor's name)  
*Rhonda Dunn* (Signature)  
May 4, 2004 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/980,040	11/27/2001	Sammy Mok	NNEX0003	9626

TITLE OF INVENTION: CONSTRUCTION STRUCTURES AND MANUFACTURING PROCESSES FOR INTEGRATED CIRCUIT WAFER PROBE CARD ASSEMBLIES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$665	\$0	\$665	07/07/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
DUVERNE, JEAN F	2839	439-055000

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NanoNexus, Inc.

San Jose, CA

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Michael A. Glenn, Reg. No. 30,176 May 4, 2004

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In re Application of: Mok et al.

Docket No.: NNEX0003

Serial No.: 09/980,040

Art Unit: 2839

Filed: 27 December 2001

Examiner: Duverne, Jean F.

Title: Construction Structures and Manufacturing Processes for Integrated Circuit Wafer Probe Card Assemblies

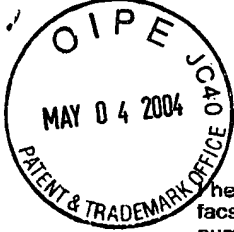
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2. Part B – Fee(s) Transmittal (1 sheet)



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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of: Mok**

**Docket No.: NNEX0003**

**Serial No. : 09/980,040**

**Art Unit: 2839**

**Filed: 11/27/2001**

**Examiner: Jean Duverne**

**Title: Construction Structures and Manufacturing Processes for Integrated Circuit Wafer  
Probe Card Assemblies**

May 4, 2004

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Respectfully submitted,

*Michael A. Glenn*  
Michael A. Glenn  
Reg. No. 30,176

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